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INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

(Use several sheets if necessary)

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Applicant M.J. Fairhurst et al.	
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U.S. PATENT DOCUMENTS

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EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

TMB	American National Standard for Information Technology. "Serial Storage Architecture- Transport Layer 2 (SSA-TL2)", Revision 5b, April 1, 1997, pp 1-107.
TMB	Blunden, M. and P. Staples. "Understanding SSA Subsystems in Your Environment". © International Business Machines Corporation 2000. 1st Ed., April 2000. pp. 1-151.
TMB	Judd, I.D., P.J. Murfet, and M.J. Palmer. "Serial Storage Architecture", *IBM Journal of Research and Development.* [online] Volume 40, Number 6, 1996. [Retrieved on 12 November 2001]. Retrieved from the Internet at <URL: http://www.research.ibm.com/journal/rd/406/judd.html>
TMB	IBM Corp. "Serial Storage Architecture: A High-Performance Open Storage Interface", [online] © International Business Machines Corporation 1997. [Retrieved on 11 November 2001]. Retrieved from the Internet at <URL: http://ssdweb01.storage.ibm.com/hardsoft/ssaovu.htm>
TMB	U.S. Patent Application Serial No. 09/998,370 filed on November 29, 2001, entitled "Method, System, and Program for Error Handling in a Dual Adaptor System Where One Adaptor is a Master", invented by D.R. Kahler, K.A. Nielsen and M.P. Vageline.

EXAMINER	DATE CONSIDERED 7/21/04

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